



LIGO Laboratory / LIGO Scientific Collaboration

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LIGO

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ECO for the GS-13 Socket Boards D0902011 rev. v3

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Distribution of this document:
LIGO Scientific Collaboration

This is an internal working note
of the LIGO Laboratory.

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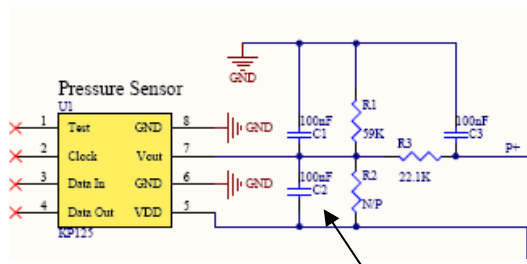
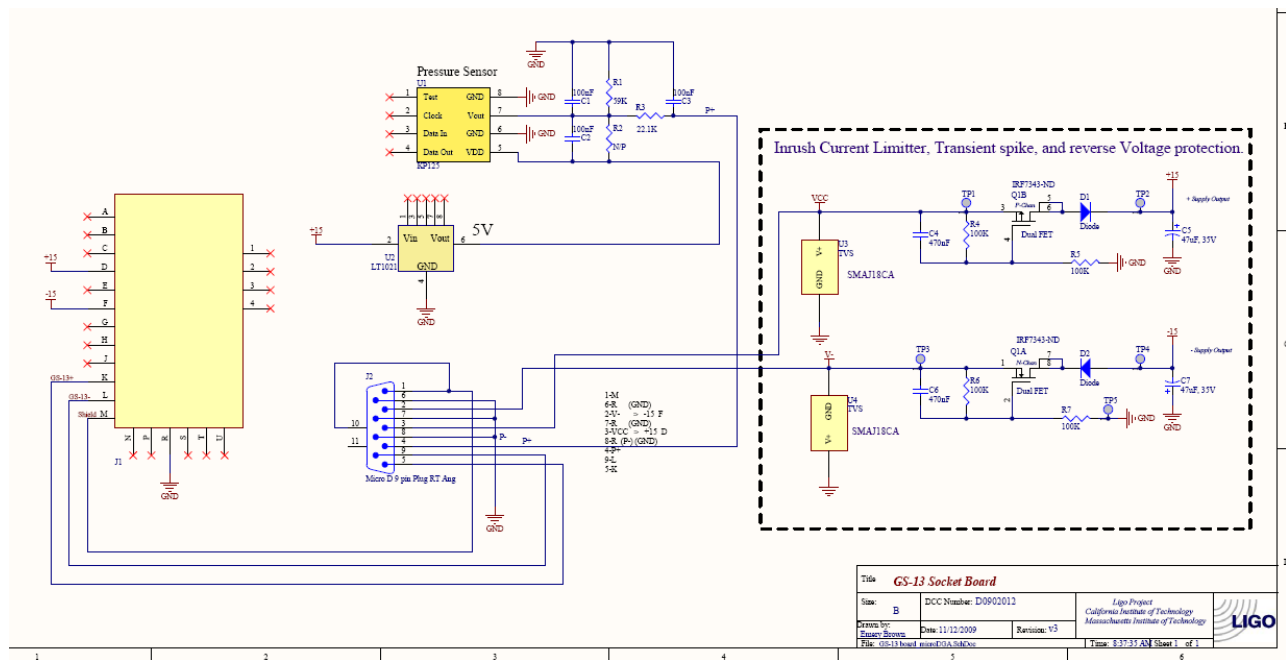
<http://www.ligo.caltech.edu/>

1 Introduction

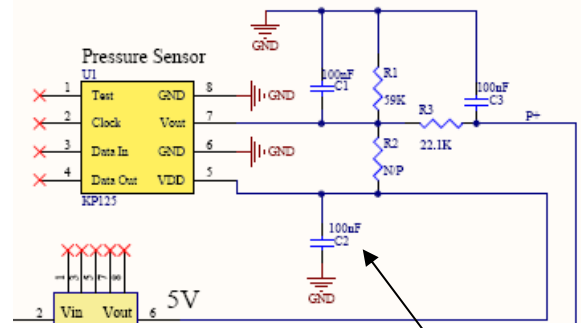
The purpose of this form is to document the changes that should be performed on a GS-13 Socket Board whose version on the PCB is D0902011-v3.

2 Schematic changes

The only change to this board is that capacitor C2 should be moved so that the incoming voltage reference is shunted directly to ground, instead of being shunted to the Pressure output. Here's the original schematic:



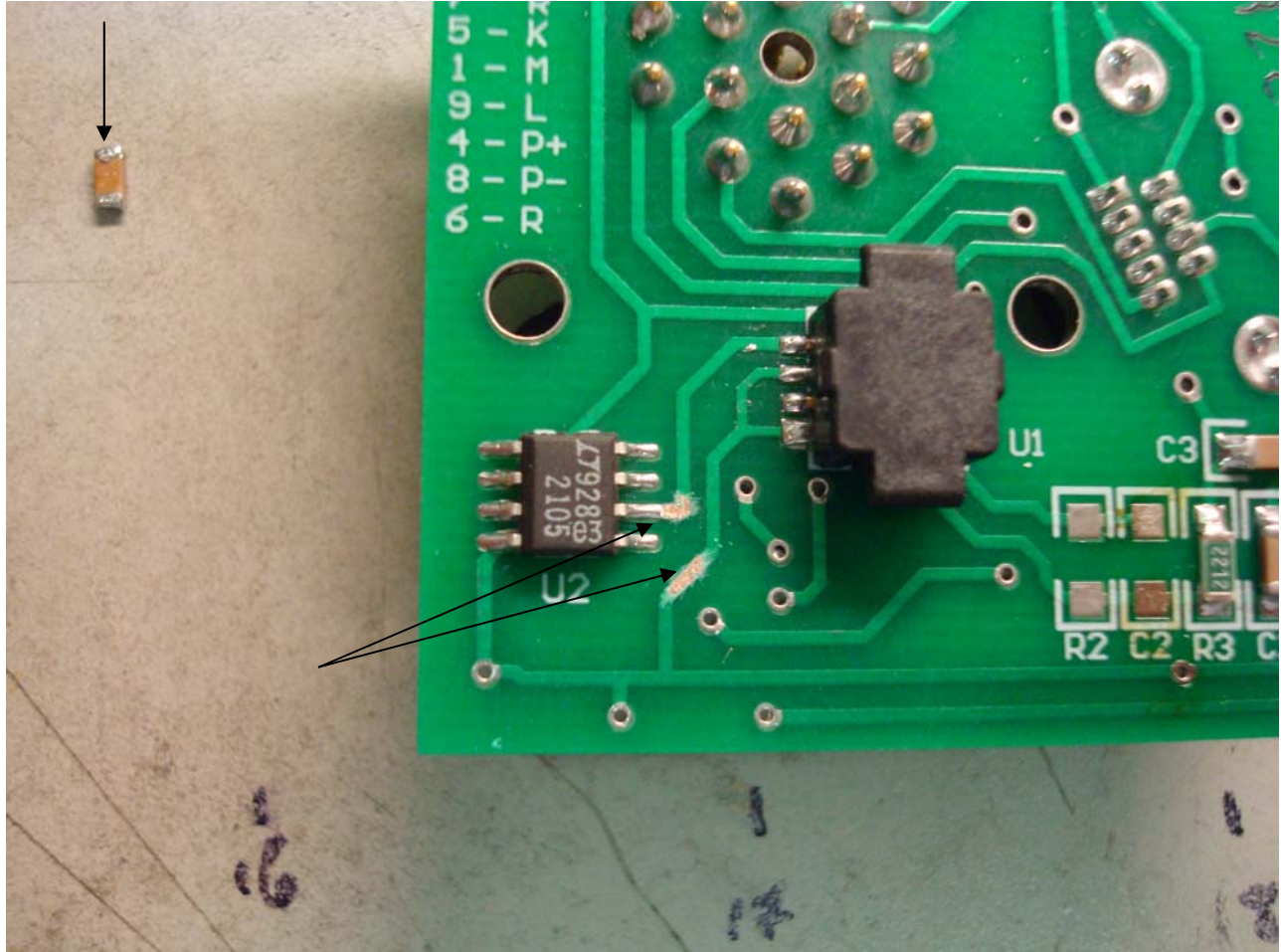
C2 should be moved from here...



...to here.

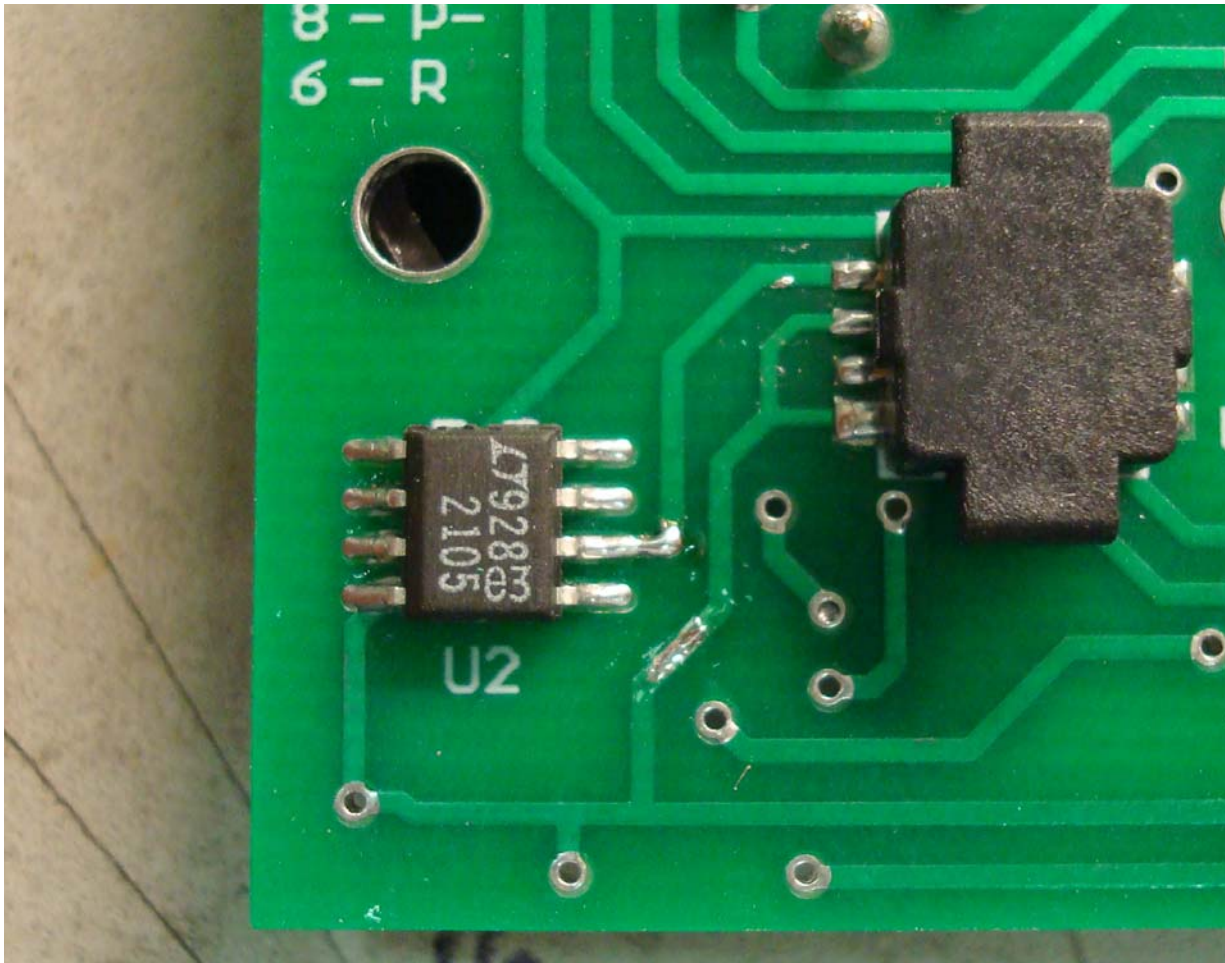
Here are a few pictures of how this can be accomplished on the v3 board:

Here, C2 (on the left of the picture) has been removed from its original position, and the two traces near U2 have been carefully scraped to remove the solder mask...



Care should be exercised to not scrape all the way through the copper traces.

The two traces are lightly tinned, to ensure their cleanliness (tinning will not stick to dirty traces, or ones that still have soldermask on them)...



Then C2 is placed, straddling the gap, being sure to make the solder joints neat and secure, and that they haven't shorted anything.

