C1 100n
VCC
GND
L1 1uH
TP1
VCC1
C2 100n
C3 10u
GND
GND
TP2
TP3

U1 SN65HDV12
VDD 8
RO 1
RE 2
DE 3
DI 4
$\overline{DO}/\overline{RI}$ 7
DO/RI 6
GND 5
GND
GND
GND

R1 20
R2 49.9
C4 330p
R3 20
R4 49.9

GND
W1
R5 10K
VCC

TP4
TP5
GND
TP6

C5 100n
C6 10u
VCC1
GND

U2 SN65HDV12
VDD 8
RO 1
RE 2
DE 3
DI 4
$\overline{DO}/\overline{RI}$ 7
DO/RI 6
GND 5
GND
GND

R6 20
R7 49.9
C7 330p
R8 20
R9 49.9

TP7
TP8

GND
W2
R10 10K
VCC

TP9
GND
TP10

C8 100n
C9 10u
VCC1
GND

U3 SN65HDV12
VDD 8
RO 1
RE 2
DE 3
DI 4
$\overline{DO}/\overline{RI}$ 7
DO/RI 6
GND 5
GND
GND

R11 20
R12 49.9
C10 330p
R13 20
R14 49.9

TP11
TP12

GND
W3
R15 10K
VCC

TP13
VCC
C12 220u
C11 10u
GND
TP17

TP14
GND
TP15
TP16

C13 100n
C14 10u
VCC1
GND

U4 SN65HDV12
VDD 8
RO 1
RE 2
DE 3
DI 4
$\overline{DO}/\overline{RI}$ 7
DO/RI 6
GND 5
GND
GND

R16 20
R17 49.9
C15 330p
R18 20
R19 49.9

TP18
TP19
GND

GND
W4
R20 10K
VCC

J1 787204-5
1 14 2 15 3 16 4 17 5 18 6 19 7 20 8 21 9 22 10 23 11 24 12 25 13
26 27
GND
GND
GND
VCC

J2 787204-5
1 14 2 15 3 16 4 17 5 18 6 19 7 20 8 21 9 22 10 23 11 24 12 25 13
26 27
GND

Title
Digital IO: RS422 Prototype

Size B
Number D070211
Revision A

Date: 6/6/2007
Sheet 1 of 1
File: C:\User\..\RS422io1.SchDoc
Drawn By: Daniel Sigg