



LIGO Laboratory / LIGO Scientific Collaboration

LIGO-T1100472-v3

LIGO

July 11, 2012

ADC and DAC Channel Usage for ISC

Rich Abbott, Peter Fritschel

Distribution of this document:
LIGO Scientific Collaboration

This is an internal working note
of the LIGO Laboratory.

California Institute of Technology
LIGO Project – MS 18-34
1200 E. California Blvd.
Pasadena, CA 91125
Phone (626) 395-2129
Fax (626) 304-9834
E-mail: info@ligo.caltech.edu

Massachusetts Institute of Technology
LIGO Project – NW22-295
185 Albany St
Cambridge, MA 02139
Phone (617) 253-4824
Fax (617) 253-7014
E-mail: info@ligo.mit.edu

LIGO Hanford Observatory
P.O. Box 159
Richland WA 99352
Phone 509-372-8106
Fax 509-372-8137

LIGO Livingston Observatory
P.O. Box 940
Livingston, LA 70754
Phone 225-686-3100
Fax 225-686-7189

<http://www.ligo.caltech.edu/>

1 Purpose

This document lists the specific ADC and DAC channels used within the ISC I/O expansion chassis. In the following tables, the entries given in the ‘Signal’ column are *not* meant to be the exact DAQ channel name for that signal (though they may be); rather the entries are intended as descriptors to identify the actual hardware channel that is connected to a given ADC/DAC channel.

The reference document for the actual DAQ channel names is [T1000264](#), *List of ISC Photodetectors in Advanced LIGO*.

2 ASC-IO I/O Chassis

Card	AA/AI conn.	ADC/DAC Chns.	Signal			
ADC 0	DB9_1	1	IMC_WFS_A	Seg 1	Q-phase	
		2			I-phase	
		3		Seg 2	Q-phase	
		4			I-phase	
	DB9_2	5		IMC_WFS_B	Seg 3	Q-phase
		6				I-phase
		7			Seg 4	Q-phase
		8				I-phase
	DB9_3	9	IMC_WFS_B	Seg 1	Q-phase	
		10			I-phase	
		11		Seg 2	Q-phase	
		12			I-phase	
	DB9_4	13		IMC_WFS_B	Seg 3	Q-phase
		14				I-phase
		15			Seg 4	Q-phase
		16				I-phase
	DB9_5	17-20	IMC_WFS_A	Segs 1-4	DC Outputs of WFS	
	DB9_6	21-24	IMC_WFS_B	Segs 1-4		
	DB9_7	25	IMC Servo, I monitor (error signal)			
		26	IMC Servo, Fast monitor (MC_F)			
		27	IMC Servo, Slow monitor (MC_L)			
		28	Unused			
	DB9_8	29-30	Unused			
		31	Duotone (DAC)			
		32	Duotone			

Card	AA/AI conn.	ADC/DAC Chs.	Signal		
ADC 1	DB9_1	1-4	IO QPD: SM2 Transmission		
	DB9_2	5-8	IO QPD: MC2 Transmission		
	DB9_3	9	PD 1: post-EOM		4 ch Generic PD interface: PSL/IO table
		10	PD 2: post-power control		
		11	PD 3: unused		
		12	PD 4: unused		
	DB9_4	13	PD 1: SM1 Transmission		4 ch Generic PD interface: IOT1
		14	PD 2: IMC_PDH DC out		
		15-16	PD 3,4: unused		
	DB9_5	17	PD 1: SM2 Fwd Trans		4 ch Generic PD interface: IOT2
		18	PD 2: SM2 Bwd Trans		
		19-20	PD 3,4: unused		
	DB9_6	21-24	Unused		
	DB9_7	25-28	Unused		
DB9_8	29-32	Unused			

Card	AA/AI conn.	ADC/DAC chan.	Signal			
ADC 2	DB9_1	1	ASC_REFL(AIR)_A_RF9	Seg 1	Q-phase	
		2			I-phase	
		3		Seg 2	Q-phase	
		4			I-phase	
	DB9_2	5		ASC_REFL(AIR)_A_RF45	Seg 3	Q-phase
		6				I-phase
		7			Seg 4	Q-phase
		8				I-phase
	DB9_3	9	ASC_REFL(AIR)_B_RF9		Seg 1	Q-phase
		10				I-phase
		11			Seg 2	Q-phase
		12				I-phase
	DB9_4	13		ASC_REFL(AIR)_B_RF45	Seg 3	Q-phase
		14				I-phase
		15			Seg 4	Q-phase
		16				I-phase
	DB9_5	17	ASC_REFL(AIR)_A_RF9		Seg 1	Q-phase
		18				I-phase
		19			Seg 2	Q-phase
		20				I-phase
	DB9_6	21		ASC_REFL(AIR)_A_RF45	Seg 3	Q-phase
		22				I-phase
		23			Seg 4	Q-phase
		24				I-phase
	DB9_7	25	ASC_REFL(AIR)_B_RF9		Seg 1	Q-phase
		26				I-phase
		27			Seg 2	Q-phase
		28				I-phase
	DB9_8	29		ASC_REFL(AIR)_B_RF45	Seg 3	Q-phase
		30				I-phase
		31			Seg 4	Q-phase
		32				I-phase

Card	AA/AI conn.	ADC/DAC chan.	Signal			
ADC 3	DB9_1	1	Spare Set of WFS Channels (1)	Seg 1	Q-phase	
		2			I-phase	
		3		Seg 2	Q-phase	
		4			I-phase	
	DB9_2	5		Spare Set of WFS Channels (1)	Seg 3	Q-phase
		6				I-phase
		7			Seg 4	Q-phase
		8				I-phase
	DB9_3	9	Spare Set of WFS Channels (2)	Seg 1	Q-phase	
		10			I-phase	
		11		Seg 2	Q-phase	
		12			I-phase	
	DB9_4	13		Spare Set of WFS Channels (2)	Seg 3	Q-phase
		14				I-phase
		15			Seg 4	Q-phase
		16				I-phase
	DB9_5	17-20	Unused			
	DB9_6	21-24	Unused			
	DB9_7	25-28	Unused			
	DB9_8	29-32	Unused			

Card	AA/AI conn.	ADC/DAC chan.	Signal			
ADC 4	DB9_1	1	ASC_AS(AIR)_A_RF45	Seg 1	Q-phase	
		2			I-phase	
		3		Seg 2	Q-phase	
		4			I-phase	
	DB9_2	5		ASC_AS(AIR)_A_RF36	Seg 3	Q-phase
		6				I-phase
		7			Seg 4	Q-phase
		8				I-phase
	DB9_3	9	ASC_AS(AIR)_B_RF45		Seg 1	Q-phase
		10				I-phase
		11			Seg 2	Q-phase
		12				I-phase
	DB9_4	13		ASC_AS(AIR)_B_RF36	Seg 3	Q-phase
		14				I-phase
		15			Seg 4	Q-phase
		16				I-phase
	DB9_5	17	ASC_AS(AIR)_A_RF45		Seg 1	Q-phase
		18				I-phase
		19			Seg 2	Q-phase
		20				I-phase
	DB9_6	21		ASC_AS(AIR)_A_RF36	Seg 3	Q-phase
		22				I-phase
		23			Seg 4	Q-phase
		24				I-phase
	DB9_7	25	ASC_AS(AIR)_B_RF45		Seg 1	Q-phase
		26				I-phase
		27			Seg 2	Q-phase
		28				I-phase
	DB9_8	29		ASC_AS(AIR)_B_RF36	Seg 3	Q-phase
		30				I-phase
		31			Seg 4	Q-phase
		32				I-phase

Card	AA/AI conn.	ADC/DAC Chs.	Signal	
ADC 5	DB9_1	1-4	ASC_REFLAIR_A	DC Outputs of WFS
	DB9_2	5-8	ASC_REFLAIR_B	
	DB9_3	9-12	ASC_ASAIR_A	
	DB9_4	13-16	ASC_ASAIR_B	
	DB9_5	17-20	Spare Set 1	
	DB9_6	21-24	Spare Set 2	
	DB9_7	25-28	REFL TT1	Tip-Tilt Coil driver readbacks
	DB9_8	29-32	REFL TT2	

Card	AA/AI conn.	ADC/DAC Chs.	Signal	
ADC 6	DB9_1	1-4	ASC_POP_A (QPD)	
	DB9_2	5-8	ASC_POP_B (QPD)	
	DB9_3	9-12	ASC_AS_C (QPD)	
	DB9_4	13-16	ASC_OMC_A (QPD)	
	DB9_5	17-20	ASC_OMC_B (QPD)	
	DB9_6	21-24	ASC_OMCR_A (QPD)	
	DB9_7	25-28	ASC_OMCR_B (QPD)	
	DB9_8	29-32	Unused	

Card	AA/AI conn.	ADC/DAC Chs.	Signal	
ADC 7	DB9_1	1-4	AS TT1	Tip-Tilt BOSEM sensor signals
	DB9_2	5-8	AS TT2	
	DB9_3	9-12	AS TT3	
	DB9_4	13-16	REFL TT1	
	DB9_5	17-20	REFL TT2	
	DB9_6	21-24	AS TT1	Tip-Tilt Coil driver readbacks
	DB9_7	25-28	AS TT2	
	DB9_8	29-32	AS TT3	

Card	AA/AI conn.	ADC/DAC Chs.	Signal
DAC 0	DB9_1	1-4	AS TT1 Coil drives (UL, LL, UR, LR)
	DB9_2	5-8	AS TT2 Coil drives (UL, LL, UR, LR)
	DB9_3	9-12	AS TT3 Coil drives (UL, LL, UR, LR)
	DB9_4	13-15	Unused
		16	Duotone

Card	AA/AI conn.	ADC/DAC Chs.	Signal
DAC 1	DB9_1	1-4	REFL TT1 Coil drives (UL, LL, UR, LR)
	DB9_2	5-8	REFL TT2 Coil drives (UL, LL, UR, LR)
	DB9_3	9	IO Input beam tip-tilt PZT drive: pitch
		10	IO Input beam tip-tilt PZT drive: yaw
		11-12	Unused
	DB9_4	13-16	Unused

3 LSC I/O Chassis

Card	AA/AI conn.	ADC/DAC Chns.	Signal
ADC 0	DB9_1	1	PD 1: ALS_DIFF
		2	PD 2: ALS_COMM
		3	PD 3: LSC_REFLAIR_B
		4	PD 4: LSC_POPAIR_B
	DB9_2	5	LSC_POPAIR_A
		6	LSC_REFLAIR_A
		7	LSC_POP_A
		8	LSC_REFL_A
	DB9_3	9	LSC_ASAIR_A
		10	LSC_ASAIR_B
		11	Unused
		12	Unused
	DB9_4	13	LSC_OMC_A
		14	LSC_OMC_B
		15-16	Unused
	DB9_5	17	OMC PZT Monitor: Slow path
		18	OMC PZT Monitor: AC path
		19-20	Unused
	DB9_6	21	CM Servo, I monitor (error signal)
		22	CM Servo, Fast monitor
		23	CM Servo, Slow monitor
		24	Unused
	DB9_7	25-28	Unused
	DB9_8	29	Unused
		30	Unused
		31	Duotone (DAC)
		32	Duotone

Card	AA/AI conn.	ADC/DAC Chns.	Signal		
ADC 1	DB9_1	1	LSC_POPAIR_B	RF18	Q-phase
		2			I-phase
		3		RF90	Q-phase
		4			I-phase
	DB9_2	5	LSC_REFLAIR_B	RF27	Q-phase
		6			I-phase
		7		RF135	Q-phase
		8			I-phase
	DB9_3	9	LSC_ASAIR_B	RF18	Q-phase
		10			I-phase
		11		RF90	Q-phase
		12			I-phase
	DB9_4	13	LSC_ASAIR_A	RF45	Q-phase
		14			I-phase
		15	Unused		Q-phase
		16			I-phase
	DB9_5	17	LSC_POPAIR_A	RF9	Q-phase
		18			I-phase
		19		RF45	Q-phase
		20			I-phase
	DB9_6	21	LSC_REFLAIR_A	RF9	Q-phase
		22			I-phase
		23		RF45	Q-phase
		24			I-phase
	DB9_7	25	LSC_POP_A	RF9	Q-phase
		26			I-phase
		27		RF45	Q-phase
		28			I-phase
	DB9_8	29	LSC_REFL_A	RF9	Q-phase
		30			I-phase
		31		RF45	Q-phase
		32			I-phase

Card	AA/AI conn.	ADC/DAC Chs.	Signal
DAC 0	TBD	1	OMC PZT Dither
		2	OMC PZT Drive (feedback)
		3	Fast Shutter control
		4	Offset for ALS Common VCO
		5	CM Summing module control
		6	
		7	
		8	
		9	
		10	
		11	
		12	
		13	
		14	
		16	Duotone

4 End Stations

The first ADC card (ADC 0) is used by PEM and Photon Calibrator. The first ISC card is ADC 1.

Card	AA/AI conn.	ADC/DAC Chs.	Signal	
ADC 1	DB9_1	1-4	ASC_TRX(Y)_A	In-Vac Quad Photodiodes
	DB9_2	5-8	ASC_TRX(Y)_B	
	DB9_3	9-12	ALS_QPDX(Y)_A	
	DB9_4	13-16	ALS_QPDX(Y)_B	
	DB9_5	17-20	ALS_WFS_A	DC Outputs of WFS
	DB9_6	21-24	ALS_WFS_B	
	DB9_7	25-28	Unused	
	DB9_8	29-31	Unused	
32		Duotone		

Card	AA/AI conn.	ADC/DAC Chs.	Signal			
ADC 2	DB9_1	1	CM Servo: ALS phase-locking, I monitor			
		2	CM Servo: ALS phase-locking, Fast monitor			
		3	CM Servo: ALS phase-locking, Slow monitor			
		4	Unused			
	DB9_2	5	CM Servo: ALS PDH-locking, I monitor			
		6	CM Servo: ALS PDH-locking, Fast monitor			
		7	CM Servo: ALS PDH-locking, Slow monitor			
		8	Unused			
	DB9_3	9	PD1: REFL power mon.	4 ch. PD interface chassis: ALS Table		
		10	PD2: IR power mon			
		11	PD3: Green power mon.			
		12	PD4: BBPD			
	DB9_4	13	PD1: DC from LSC RF PD	4 ch. PD interface chassis: Field rack		
		14	PD2:			
		15	PD3:			
		16	PD4:			
	DB9_5	17	ALS_WFS_A	Seg 1	Q-phase	
		18			I-phase	
		19		Seg 2	Q-phase	
		20			I-phase	
	DB9_6	21		ALS_WFS_B	Seg 3	Q-phase
		22				I-phase
		23			Seg 4	Q-phase
		24				I-phase
	DB9_7	25	ALS_WFS_B		Seg 1	Q-phase
		26				I-phase
		27			Seg 2	Q-phase
		28				I-phase
	DB9_8	29		ALS_WFS_B	Seg 3	Q-phase
		30				I-phase
		31			Seg 4	Q-phase
		32				I-phase

Card	AA/AI conn.	ADC/DAC Chs.	Signal
DAC 0	DB9_1	1	MCL PZT Tip-tilt 1: pitch
		2	MCL PZT Tip-tilt 1: yaw
		3	MCL PZT Tip-tilt 2: pitch
		4	MCL PZT Tip-tilt 2: yaw
	DB9_2	5-8	Unused
	DB9_3	9-12	Unused
	DB9_4	13-15	Unused
		16	Duotone ?

5 Summary

Below is a summary of the number of I/O cards, unused channels, and available I/O slots for the ISC I/O Expansion Chassis. This assumes there are a total of 10 slots available in the I/O chassis for ADC and/or DAC cards. For the unused ADC channel column, the number in parentheses is the subset of these channels that are available via the Anti-Alias (AA) chassis on open DB9 connectors; the other channels are found on AA DB9 connectors

I/O Chassis	# ADC cards	# DAC cards	Unused ADC chans	Unused DAC chans	Available I/O slots
Vertex: ASC-IO	8	2	35 (32)	9	0
Vertex: LSC	2	1	15 (4)	10	7
End X	2	1	13 (4)	11	5
End Y	2	1	13 (4)	11	5
Totals	14	5	76 (44)	41	17

At the end stations, the I/O Expansion Chassis is shared with PEM. PEM has one ADC card, of which 14 channels are used, and one DAC card. The DAC card provides 8 channels (18 bit) of general purpose test outputs. The ‘available slots’ number includes these PEM cards.