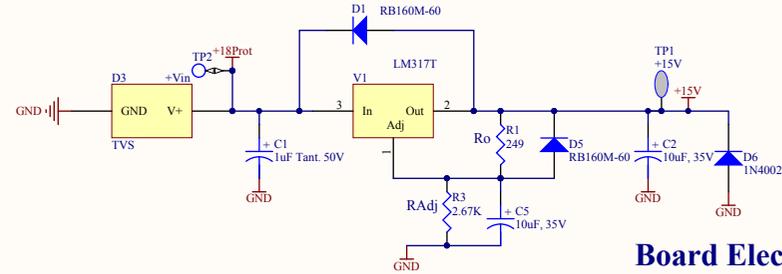
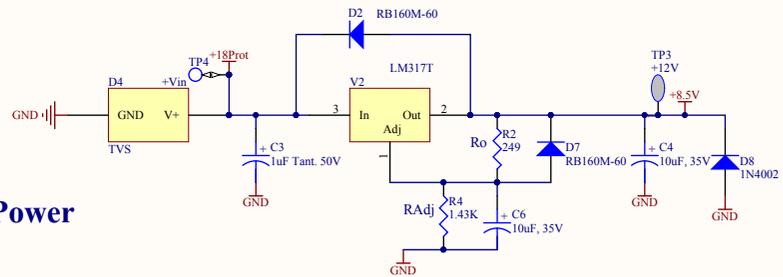


$$LM317 VOUT = 1.25 \times (1 + R_{Adj}/R_o) + (46\mu A \times R_{Adj})$$

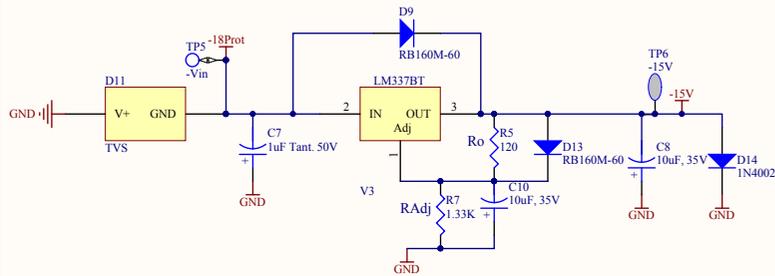


$$LM317 VOUT = 1.25 \times (1 + R_{Adj}/R_o) + (46\mu A \times R_{Adj})$$

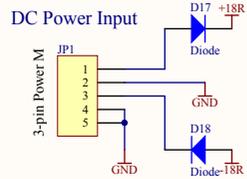
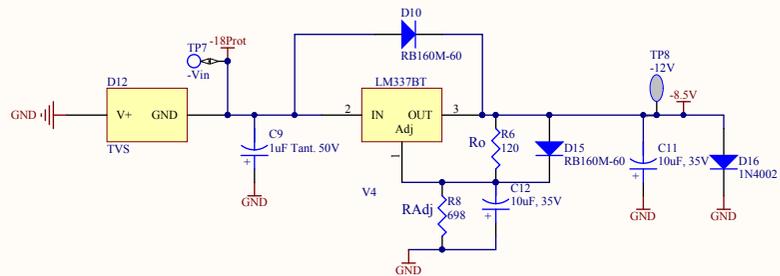


### Board Electronics Power

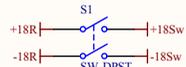
$$LM337 VOUT = -[1.25 \times (1 + R_{Adj}/R_o) + (65\mu A \times R_{Adj})]$$



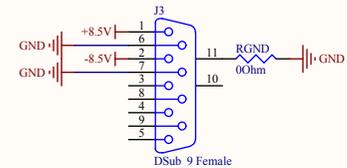
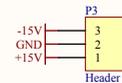
$$LM337 VOUT = -[1.25 \times (1 + R_{Adj}/R_o) + (65\mu A \times R_{Adj})]$$



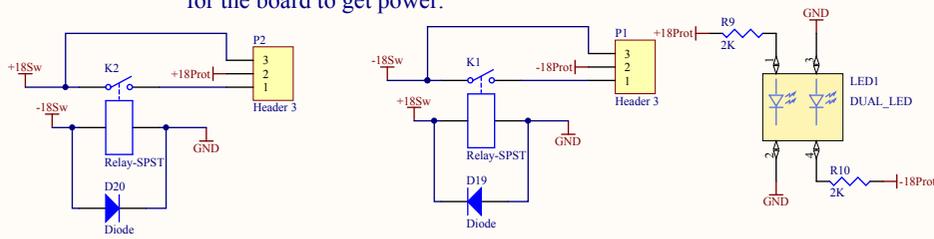
### Power Switch



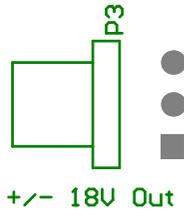
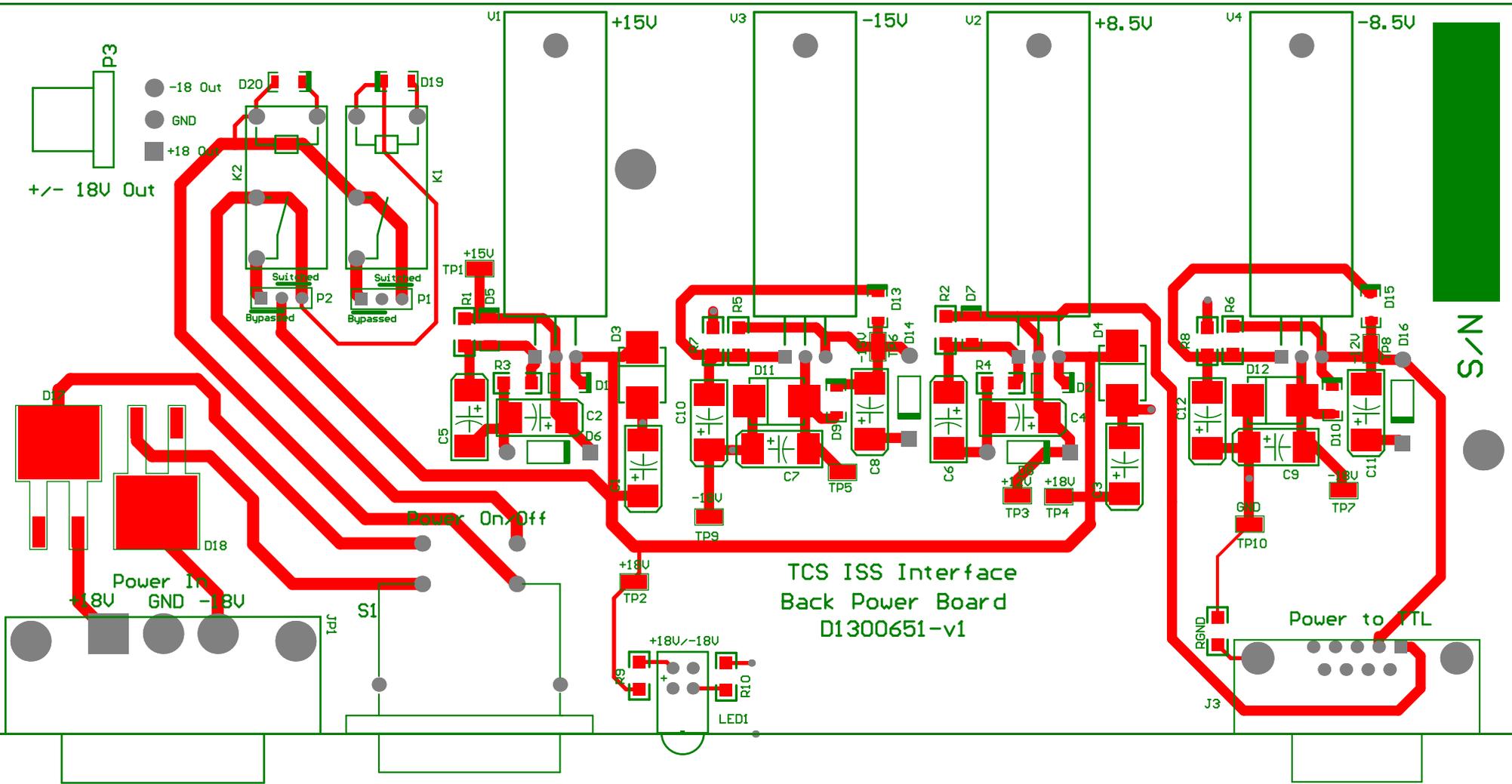
### Power to TTL Box



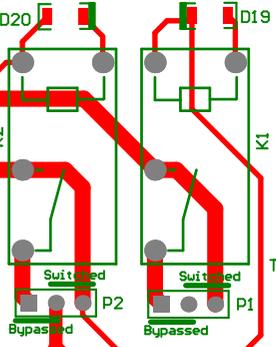
Both power supplies must be present for the board to get power.



Title <b>aLIGO TCS ISS Chassis Power Back Board</b>			
Size: <b>B</b>	DCC Number: D1300651	Ligo Project California Institute of Technology Massachusetts Institute of Technology	
Drawn by: <b>Ben Abbott</b>	Date: 7/29/2013	Revision: v1	
File: C:\restored\Ben\AOS\TCS\Snuff\TCS ISS Chassis\ISS Back Power Board\TCS ISS Back Power Board_Schematic_Sat.Dwg			

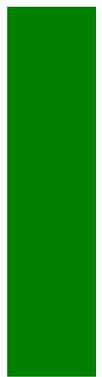


- -18 Out
- GND
- +18 Out



U1 +15V U3 -15V U2 +8.5V U4 -8.5V

TCS ISS Interface  
Back Power Board  
D1300651-v1



S/N

Power to TTL

J3

Power On/Off

Power In  
+18V GND -18V

S1

+18V/-18V

LED1

TP2

+18V

TP9

-18V

TP3

+18V

TP4

+18V

TP7

-18V

TP8

+18V

TP10

GND

RGND

TP15

D16

TP16

D15

TP17

D12

TP18

D11

TP19

D10

TP20

D9

TP21

D8

TP22

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